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20792 7590 05/05/2004  
**MYERS BIGEL SIBLEY & SAJOVEC**  
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<b>Candi L. Riggs</b>	(Depositor's name)
	(Signature)
6/30/04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/891,906	06/26/2001	Robert J. Proebsting	5646-54	1428

TITLE OF INVENTION: OVERVOLTAGE PROTECTION CIRCUITS THAT UTILIZE CAPACITIVELY BOOTSTRAPPED VARIABLE VOLTAGES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	NO	\$1330	\$0	\$1330	08/05/2004
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EXAMINER	ART UNIT	CLASS-SUBCLASS
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NGUYEN, DANNY	2836	361-056000
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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

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**Integrated Device Technology, Inc.**

**Santa Clara, California**

Please check the appropriate assignee category or categories (will not be printed on the patent):  individual  corporation or other private group entity  government

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Issue Fee

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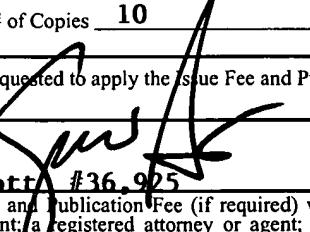
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Grant J. Scott #36.905

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